

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	83	(backplane midplane) near4 switch with (serverlets cards) and (@rlaD<"20001219" @ad<"20001219")	US-PGPUB; USPAT	OR	OFF	2005/12/05 20:29
L16	23	(backplane\$1 midplane\$1) adj2 switch\$3 and (backplane\$1 midplane\$1) near8 chassis and (@rlaD<"20001219" @ad<"20001219")	US-PGPUB; USPAT	OR	OFF	2005/12/05 20:37
L21	6	(bus midplane backplane) adj2 expander\$1 with switch\$3 and (@rlaD<"20001219" @ad<"20001219")	US-PGPUB; USPAT	OR	OFF	2005/12/05 21:22
L13	75	(backplane midplane) near4 switch near4 (modules cards) and (@rlaD<"20001219" @ad<"20001219")	US-PGPUB; USPAT	OR	OFF	2005/12/05 21:24
L20	15	line adj (modules cards) near7 (bus midplane backplane) adj2 interface and (@rlaD<"20001219" @ad<"20001219")	US-PGPUB; USPAT	OR	OFF	2005/12/05 21:31
L32	20	line adj card\$1 near4 processors same (backplane\$1 midplane\$1 bus\$2) and (@rlaD<"20001219" @ad<"20001219")	US-PGPUB; USPAT	OR	OFF	2005/12/05 21:37
L34	7	line adj (module card) near4 processors and line adj (module\$1 card\$1) with (backplane\$1 midplane\$1 bus\$2) and (@rlaD<"20001219" @ad<"20001219")	US-PGPUB; USPAT	OR	OFF	2005/12/05 21:45